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Application Number	TBD
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First Named Inventor	Tran
Art Unit	TBD
Examiner Name	TBD
Attorney Docket Number	TI-36252

U.S. PATENT DOCUMENTS					
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	A	JUNG, S., et al., "Packaging Consideration for 1 Gbps Si CMOS Optical Driver," IEEE (2000) pp. 882-883.			
	B	FAN, J., et al., "Quantifying Decoupling Capacitor Location," IEEE (February 2000) pp. 761-766.			
	C	PEIFFER, J., et al., "Electrical Performance Advantages of Ultra-Thin Dielectric Materials Used for Power-Ground Cores in High-Speed, Multilayer Printed Circuit Boards," Manuscript of Paper for IPC Expo 2003 (March 25-27, 2003) pp. 1-13.			

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